



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
Honeywell Case No. H0004488-1600  
(MBHB Case No. 03-814)

In application of:

Roy M. Carlson

Serial No.: 10/759,913

Filed: January 15, 2004

For: Radiation Hardening of Logic Circuitry  
Using A Cross-Enabled, Interlocked Logic  
System and Method

Examiner: To Be Assigned

Group Art Unit: To Be Assigned

Confirmation No. 8044

Mail Stop: Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

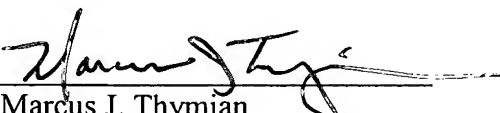
**TRANSMITTAL LETTER**

Dear Sir:

In regard to the above identified application:

1. We are transmitting herewith the attached:
  - a. Transmittal (in duplicate);
  - b. First Supplemental Information Disclosure Statement (2 sheets);
  - c. Form PTO-1449 (1 sheet);
  - d. One (1) Cited Reference (B1); and
  - e. Honeywell Return Receipt Postcard.
2. Please charge any additional fees or credit overpayment to Deposit Account No.13-2490. A duplicate copy of this sheet is enclosed.
3. CERTIFICATE OF MAILING UNDER 37 CFR § 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described in paragraph 1 hereinabove, are being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. box 1450, Alexandria, VA 22313-1450 on this 2<sup>nd</sup> day of February, 2005.

Respectfully submitted,

By:   
Marcus J. Thymian  
Reg. No. 43,954

Dated: February 2, 2005



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Honeywell Case No. H0004488-1600

(MBHB Case No. 03-814)

In application of:

Roy M. Carlson

Serial No.: 10/759,913

Filed: January 15, 2004

For: Radiation Hardening of Logic Circuitry  
Using A Cross-Enabled, Interlocked Logic  
System and Method

Examiner: To Be Assigned

Group Art Unit: To Be Assigned

Confirmation No. 8044

**FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Mail Stop: Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. Section 1.97 - 1.99, the Applicant wishes to make the following references of record in the above-identified application. This First Supplemental Information Disclosure Statement is in compliance with the continuing duty of candor as set forth in 37 C.F.R. Section 1.56. The references cited below are also listed on the enclosed PTO Form 1449.

In the judgment of the undersigned, portions of the listed references may be material to the Examiner's consideration of the presently pending claims. However, the references have not been reviewed in sufficient detail to make any other representation and, in particular, no representation is intended as to the relative relevance between references, whether cited in this or prior statements. This statement is not a representation that the listed references have effective dates early enough to be "prior art" within the meaning of 35 U.S.C. Section 102 or Section 103.

**Other Documents – Including Author, Title, Date, Pertinent Pages, Etc.**

1. "Overview: Single Event Upset Design Techniques for UTMIC's RadHard MSI Logic Family", <http://ams.aeroflex.com/ProductFiles/AppNotes/MSI/seu1.pdf>, printed February 1, 2005, pp. 1-6.

In accordance with MPEP Sections 609 and 707.05(b), it is requested the document cited (including any cited in applicant's specification which is not repeated on the attached Form PTO-1449) be given thorough consideration and that it be cited of record in the prosecution history of the present application by initialing on Form PTO-1449. Such initialing is requested even if the Examiner does not consider a cited document to be sufficiently pertinent to use in a rejection, or otherwise does not consider it to be prior art for any reason, or even if the Examiner does not believe that the guidelines for citation have been fully complied with. This is requested so that each document becomes listed on the face of the patent issuing on the present application.

Respectfully submitted,

Date: February 2, 2005

By:

A handwritten signature in black ink, appearing to read "Marcus J. Thymian", is written over a horizontal line.

Marcus J. Thymian

Reg. No. 43,954

**McDonnell, Boehnen Hulbert & Berghoff**

300 South Wacker Drive

Suite 3100

Chicago, IL 60606

Form PTO-1449 U.S. Department of Commerce  
Patent and Trademark Office

Honeywell Ref. No.  
H0004488-1600

Serial No.  
10/759,913

MBHB Case No. 03-814

Applicant: Katti, et al.

Filing Date:  
January 15, 2004

Group: TBA

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

**U.S. PATENT DOCUMENTS**

Examiner Initial	No.	Document Number	Date	Name	Class	Subclass	Filing Date
------------------	-----	-----------------	------	------	-------	----------	-------------

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	No.	Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

**OTHER DOCUMENTS - Including Author, Title, Date, Pertinent Pages, Etc.**

Examiner Initial	No.	
	B1.	"Overview: Single Event Upset Design Techniques for UPMC's RadHard MSI Logic Family", <a href="http://ams.aeroflex.com/ProductFiles/AppNotes/MSI/seu1.pdf">http://ams.aeroflex.com/ProductFiles/AppNotes/MSI/seu1.pdf</a> , printed February 1, 2005, pp. 1-6.

**Examiner**

**Date Considered**

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with any communication.